TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

# TA7630P

Dual. Volume/Balance/Tone (Bass/Treble) DC Control IC

The TA7630P is DC controlled dual volume, balance, tone (Bass, treble) IC. As these dual channels are constructed on one chip, this IC is excellent in pair characteristic.

It is suitable for automobile stereo, radio cassette, music center, TV multiplex sound receiver and remote controlled applications.

#### Features

- Wide power supply voltage range: Single supply V<sub>CC</sub> (opr) = 8~14 V (Ta = 25°C) Dual supply V<sub>CC</sub> - V<sub>EE</sub> (opr) = ±4~±7 V (Ta = 25°C)
- Wide volume control range:  $V_R = 80 dB$  (typ.)
- Excellent cross talk: C.T. = 70dB (typ.)
- Stable for temperature drift.
- Wide tone control range

Control range:  $V_B = 10dB \text{ (typ.)} \text{ (f} = 1 \text{ kHz} \rightarrow 100 \text{ Hz})$  $V_T = 12dB \text{ (typ.)} \text{ (f} = 1 \text{ kHz} \rightarrow 20 \text{ kHz})$ 

#### **Block Diagram**







### **Pin Connection**

Pin No.	Symbol	Explanation
1	V <sub>EE</sub>	Negative power supply
2	INPUT-1	Input channel-1
3	T <sub>H (1)</sub>	Treble turning frequency setting.
4	T <sub>L (1)</sub>	Bass turning frequency setting.
5	REF CONT	Reference control
6	OUTPUT-1	Output channel-1
7	BAL	Balance control
8	VOL	Volume control
9	BASS	Bass control
10	TRBL	Treble control
11	OUTPUT-2	Output channel-2
12	V <sub>CC</sub>	Power supply
13	T <sub>L (2)</sub>	Bass turning frequency setting
14	T <sub>H (2)</sub>	Treble turning frequency setting
15	INPUT-2	Input channel-2
16	REF SIG	Reference signal

### Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	14	V
Power dissipation	P <sub>D</sub> (Note)	750	mW
Operating temperature	T <sub>opr</sub>	-25~75	°C
Storage temperature	T <sub>stg</sub>	-55~150	°C

Note: Derated above  $Ta = 25^{\circ}C$  in the proportion of 6 mW/°C.

# Electrical Characteristics (unless otherwise specified, $V_{CC}$ = 6 V, $V_{EE}$ = -6 V, f = 1 kHz, Ta = 25°C)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Quiescent current	I <sub>CCQ (1)</sub>	_	V <sub>CC</sub> , V <sub>EE</sub> = ±4 V VOL/BAL/BASS/TRBL SW <sub>1~4</sub> : B	_	11	17	mA
	I <sub>CCQ (2)</sub>	_	VOL/BAL/BASS/TRBL SW1~4: B	10	18	25	
Maximum input voltage	V <sub>in</sub>	_	VOL/BAL/BASS/TRBL SW <sub>1~4</sub> : B VOL SW4: A, THD = 1%	_	_	1	Vrms
Maximum output voltage	V <sub>out</sub>	_	BASS/TRBL/BAL SW <sub>1</sub> , SW <sub>2</sub> , SW <sub>3</sub> : B VOL SW <sub>4</sub> : A, THD = 1%	1	_	_	Vrms
Voltage gain	Gv	_	V <sub>in</sub> = 1 Vrms BASS/TRBL/BAL SW <sub>1~3</sub> : B VOL SW <sub>4</sub> : A	-0.5	2.0	4.5	dB
Channel helenes	C.B1	_	BASS/TRBL/BAL SW <sub>1~3</sub> : B VOL SW <sub>4</sub> : A, V <sub>in</sub> = 1 Vrms	-3	0	3	dB
	C.B2	_	VOL/BAL/BASS/TRBL SW <sub>1~4</sub> : B f = 100 Hz~20 kHz, V <sub>in</sub> = 0.1 Vrms	-3.5	0	3.5	
Volume control range	V <sub>R</sub>	_	BASS/TRBL/BAL SW <sub>1~3</sub> : B, V <sub>in</sub> = 1 Vrms VOL SW <sub>4</sub> : A $\rightarrow$ C	70	80	_	dB
	V <sub>B</sub> MAX	_	VOL/BAL SW <sub>3, 4</sub> : B BASS/TRBL SW <sub>1, 2</sub> : A, V <sub>in</sub> = 1 Vrms, f = 1kHz → 100Hz	7	11	14	dB
Dass control range	V <sub>B</sub> MIN	_	VOL/BAL SW <sub>3, 4</sub> : B BASS/TRBL SW <sub>1, 2</sub> : C, V <sub>in</sub> = 1 Vrms, f = 1 kHz $\rightarrow$ 100 Hz	-15	-11.5	-7	
Treble control range	V <sub>T</sub> MAX	_	VOL/BAL SW <sub>3, 4</sub> : B BASS/TRBL SW <sub>1, 2</sub> : A, V <sub>in</sub> = 1 Vrms, f = 1 kHz~20 kHz	7	11	14	dB
	V <sub>T</sub> MIN	_	VOL/BAL SW <sub>3, 4</sub> : B BASS/TRBL SW <sub>1, 2</sub> : C, V <sub>in</sub> = 1 Vrms, f = 1 kHz~20 kHz	-20	-14	-10	
Tone error	ΔGv	_	VOL/BAL SW <sub>3, 4</sub> : B BASS/TRBL SW <sub>1, 2</sub> : C $\rightarrow$ A V <sub>in</sub> = 1 Vrms	_	6	10	dB
Total harmonic distortion	THD	_	BASS/TRBL/BAL SW1 <sub>~3</sub> : B VOL SW <sub>4</sub> : A, V <sub>out</sub> = 150 mVrms	_	0.1	0.35	%
Output noise voltage	V <sub>no</sub>	_	BASS/TRBL/BAL SW <sub>1~3</sub> : B VOL SW4: A BPF = 50 Hz~20 kHz, input open	_	130	300	μVrms
Cross talk	SEP	_	BASS/TRBL/BAL SW <sub>1~3</sub> : B VOL SW <sub>4</sub> : A, V <sub>out</sub> = 1 Vrms	_	-70	_	dB
Control terminal input	R <sub>IN</sub>	_	pin 8, 9, 10		500	_	kΩ
resistance		_	pin 7	_	200	_	

### **Test Circuit**







### **Application Circuits**

1. Single power supply



#### 2. Dual power supply



#### 3. Application circuit using diode at reference terminal



The application circuit using diode between Pin 5 and Pin 12 has the following merits.

- (1) When each control terminal is drived by high impedance, the electrolytic capacitor between terminal 5 and GND operates as the back up capacitor, so that the rise time is short at the ON-OFF repetation of supply voltage.
- (2) When the current drain into the each control terminal varies by control voltage, the voltage of terminal 5 scarcely varies. It means a stable reference voltage.

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#### 4. Quasi-loudness circuit





### **Package Dimensions**

DIP16-P-300-2.54A

Unit : mm



Weight: 1.00 g (typ.)

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